

PRODUCT SPECIFICATION **FOR LCD MODULE**

Revision: 1.0

Model No: LSC028T03-S-V1

Module Type: COG+FPC+B/L+CTP

APPROVED SIGNATURE

- ☒ Approved Product Specification only
- ☐ Approved Product Specification and Samples

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1. General Description

LSC028T03-S-V1 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit and a backlight unit. The panel size is 2.8 inch and the resolution is 240(RGB)*320, the panel can display up to 262k colors.

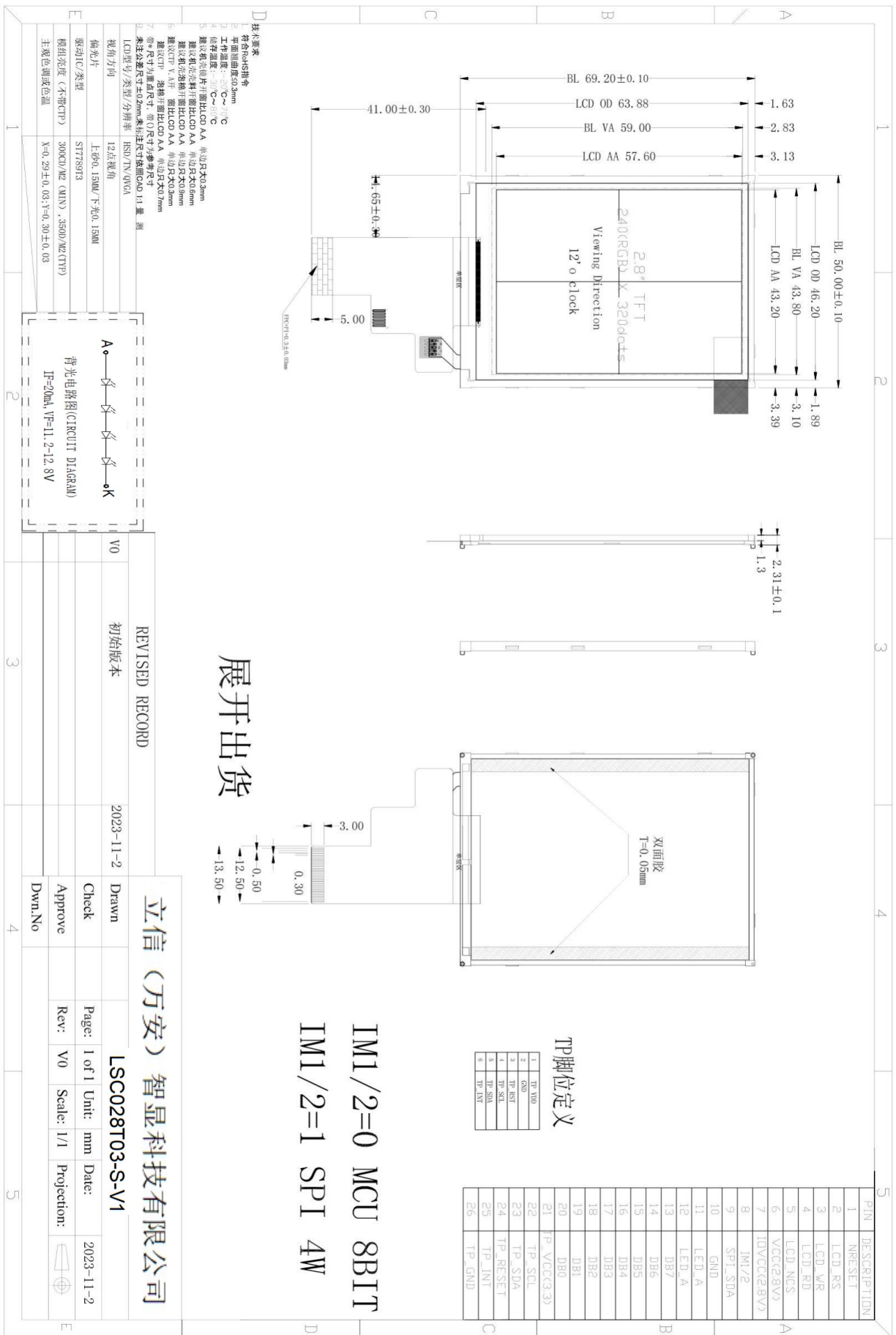
2. Physical Features

Display Mode	TFT-LCD Module
	Active matrix TFT, Transmissive type
Display Format	Graphic 240(RGB)×320 Dot-matrix
Input Data	SPI/8BIT MCU
Viewing Direction (Grayscale Inversion)	TN

3. Mechanical Specification

Item	Specification	Unit
Module size (H×V×D)	56.7×77.7×3.79	mm
Number of dots	240(RGB) × 320	pixel
Active area (H×V)	43.2×57.6	mm

4. Outline Dimension



5. Absolute Maximum Ratings

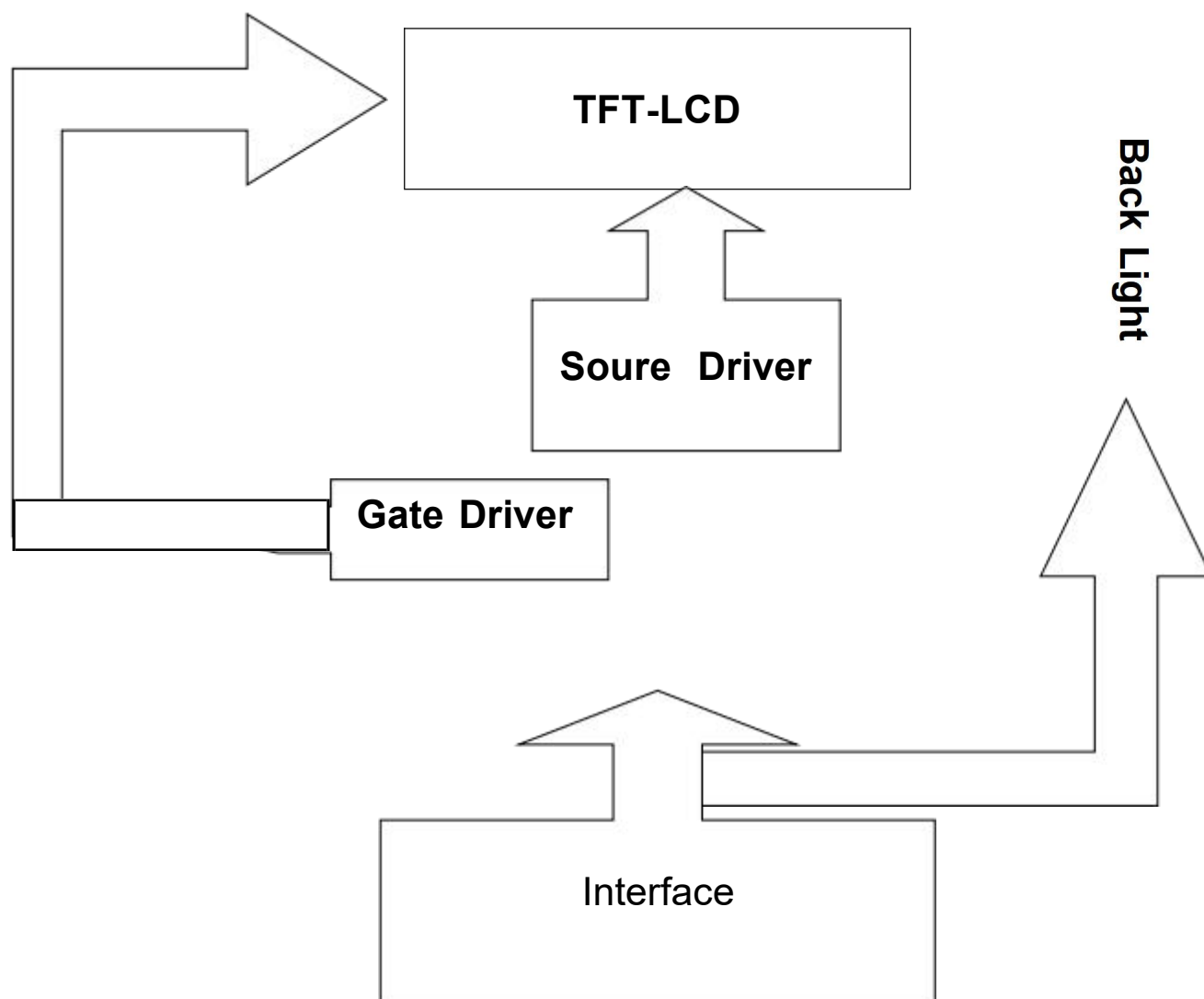
Item	Symbol	Min	Max	Unit	Remark
Supply voltage	VCC	-0.3	4.6	V	Note1 Note2
Supply voltage	IOVCC	-0.3	4.6	V	
Operating temperature	TOPR	-20	70	°C	
Storage temperature	TSTR	-30	80	°C	

6. Electrical Characteristics

Item		Symbol	Rating			Unit	Remark
			Min	Typ	Max		
Supply voltage		VCC	2.4	2.8	3.3	V	Note1
Supply voltage		IOVCC	1.65	1.8	3.3	V	
Input Voltage	L level	VIL	0	---	0.3*IOVCC	V	
	H level	VIH	0.7*IOVCC	---	IOVCC	V	

7. Module Function Description

7- 1. Block Diagram Of LCM



7-2. Pin Description

PIN NO.	Symbol	I/O	Description
1	RST	P	Reset pin.
2	SCL	I	Serial clock pin
3	RS	I	Command and Data select pin
4	RD	I	Read enable in 8080 MCU parallel interface. -If not used, please fix this pin at VDDI or DGND.
5	CS	I	Chip select pin
6	VCC	P	power supply
7	IOVCC	P	power supply
8	IM1/2	I	IM1/2=1,SPI interface mode; IM/2=0,MCU interface mode;
9	SDA	I	Serial data input/output PIN
10	GND	P	Ground
11	A	P	Power for LED backlight anode
12	A	P	Power for LED backlight anode
13	D7	I	Data bus,Fix to GND level when not in uuse
14	D6	I	Data bus,Fix to GND level when not in uuse
15	D5	I	Data bus,Fix to GND level when not in uuse
16	D4	I	Data bus,Fix to GND level when not in uuse
17	D3	I	Data bus,Fix to GND level when not in uuse
18	D2	I	Data bus,Fix to GND level when not in uuse
19	D1	I	Data bus,Fix to GND level when not in uuse
20	D0	I	Data bus,Fix to GND level when not in uuse
21	TP_VCC	P	power supply
22	TP_SCL	P	Serial clock pin
23	TP_SDA	I	Serial data input/output bidirection pin
24	TP_RESET	I	Reset signal pin
25	TP_INT	I	Interrupt signal
26	TP_GND	I	Ground

7-3 Timing Characteristics

7.4.3 Serial Interface Characteristics (4-line serial):

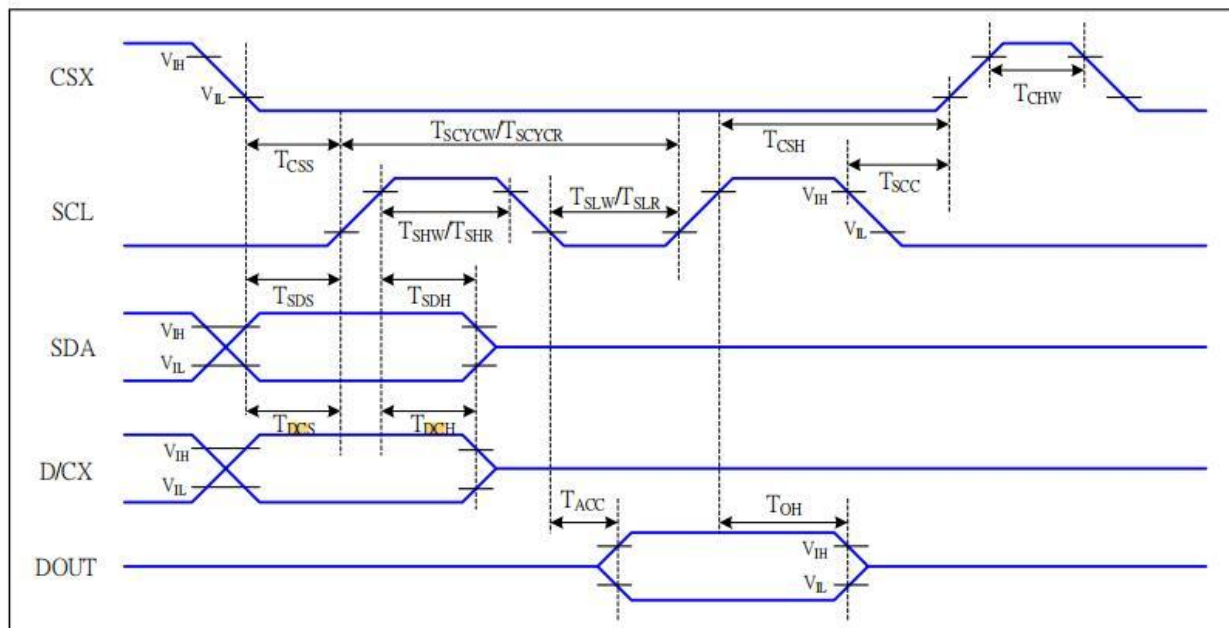


Figure 5 4-line serial Interface Timing Characteristics

$V_{DDI}=1.65$ to $3.3V$, $V_{DD}=2.4$ to $3.3V$, $AGND=DGND=0V$, $T_a=25^{\circ}C$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	16		ns	-write command & data ram
	T_{SHW}	SCL "H" pulse width (Write)	7		ns	
	T_{SLW}	SCL "L" pulse width (Write)	7		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T_{DCS}	D/CX setup time	10		ns	
	T_{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T_{SDS}	Data setup time	7		ns	
	T_{SDH}	Data hold time	7		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum $CL=30pF$
	T_{OH}	Output disable time	15	50	ns	For minimum $CL=8pF$

Table 6 4-line serial Interface Characteristics

8. Electro-Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response time		Tr +Tf	$\theta_x = \theta_y = 0$	---	16	32	ms	Note 1
Contrast Ratio		CR		---	500	---	---	Note 2
Color Chromaticity (CIE1931)	White	W x	$\theta_x = \theta_y = 0$	---	0.303	---	---	
		W y		---	0.325	---	---	
Viewing angle	θ_T		CR > 10	---	50	---	Deg.	Note 3
	θ_B			---	20	---		
	θ_L			---	45	---		
	θ_R			---	45	---		
Luminance ($I_F = 20mA$)		L			280	---	cd/m2	Note4

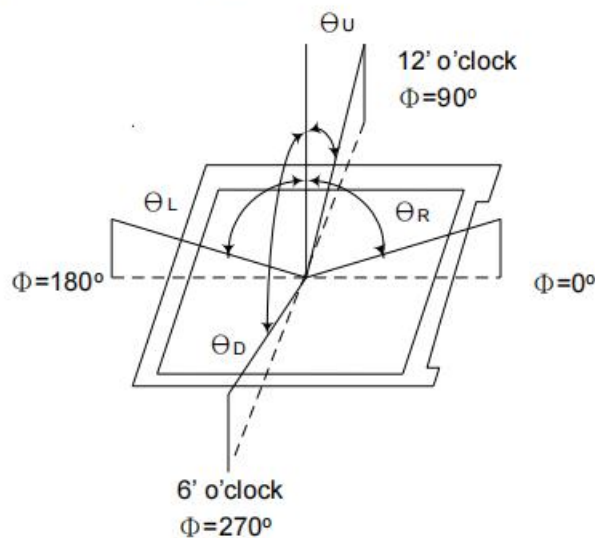
4.2 Measuring Condition

- Measuring surrounding : dark room
- Ambient temperature : $25 \pm 2^\circ\text{C}$
- 15min. warm-up time.

4.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

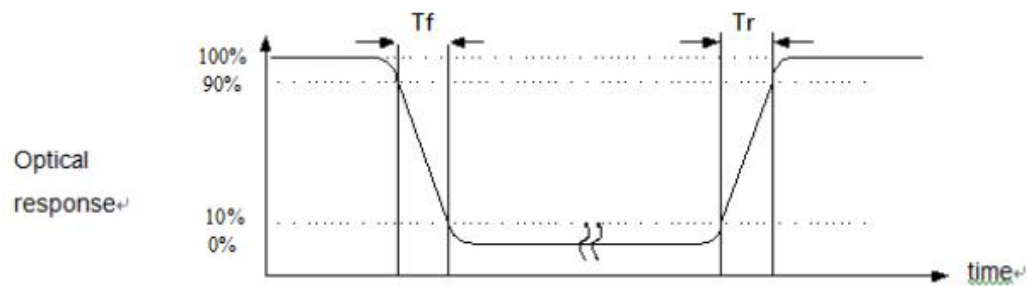
Note (1) Definition of Viewing Angle:



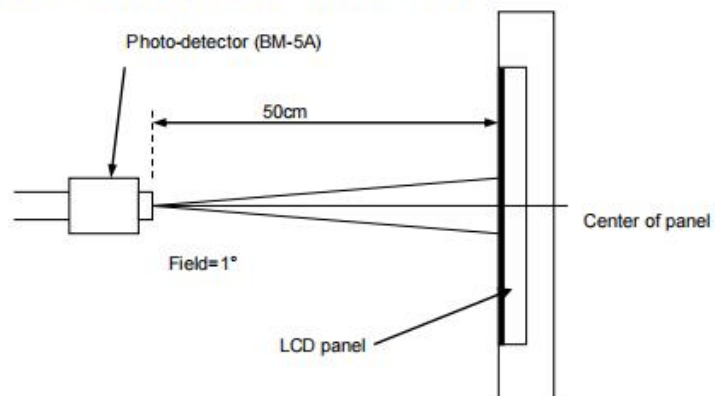
Note (2) Definition of Contrast Ratio (CR):
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

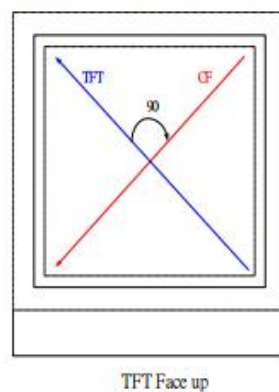
Note (3) Definition of Response Time: Sum of T_R and T_F



Note (4) Definition of optical measurement setup



Note (5) Rubbing Direction (The different Rubbing Direction will cause the different view direction).



Note(4) Backlight circuit



背光电路图(CIRCUIT DIAGRAM)

$I_F=20\text{mA}$, $V_F=11.2-12.8\text{V}$

9.Records Of Version

Version	Revise Date	Page	Content
00	2023-5-9	ALL	New released
01	2024-5-13		修改背光为窄边框