

PRODUCT SPECIFICATION **FOR LCD MODULE**

Revision: 1.0

Model No: LS028I01-S-V1

Module Type: COG+FPC+B/L

APPROVED SIGNATURE

- Approved Product Specification only
- Approved Product Specification and Samples

<u>Prepared By</u>	<u>Checked By</u>	<u>Approved By</u>

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1. General Description

LS028I01-S-V1 is a transmissive type a-Si TFT-LCD (amorphous silicon thin film transistor liquid crystal display) module, which is composed of a TFT-LCD panel, a driver circuit and a backlight unit. The panel size is 2.8 inch and the resolution is 240(RGB)*320, the panel can display up to 262k colors.

2. Physical Features

Display Mode	TFT-LCD Module
	Active matrix TFT, Transmissive type
Display Format	Graphic 240(RGB)×320 Dot-matrix
Input Data	SPI
Viewing Direction (Grayscale Inversion)	Free (IPS)

3. Mechanical Specification

Item	Specification	Unit
Module size (H×V×D)	50 ×69.2 ×2.3	mm
Number of dots	240(RGB) ×320	pixel
Active area (H×V)	43.2×57.6	mm

4. Outline Dimension

[illegible]

5. Absolute Maximum Ratings

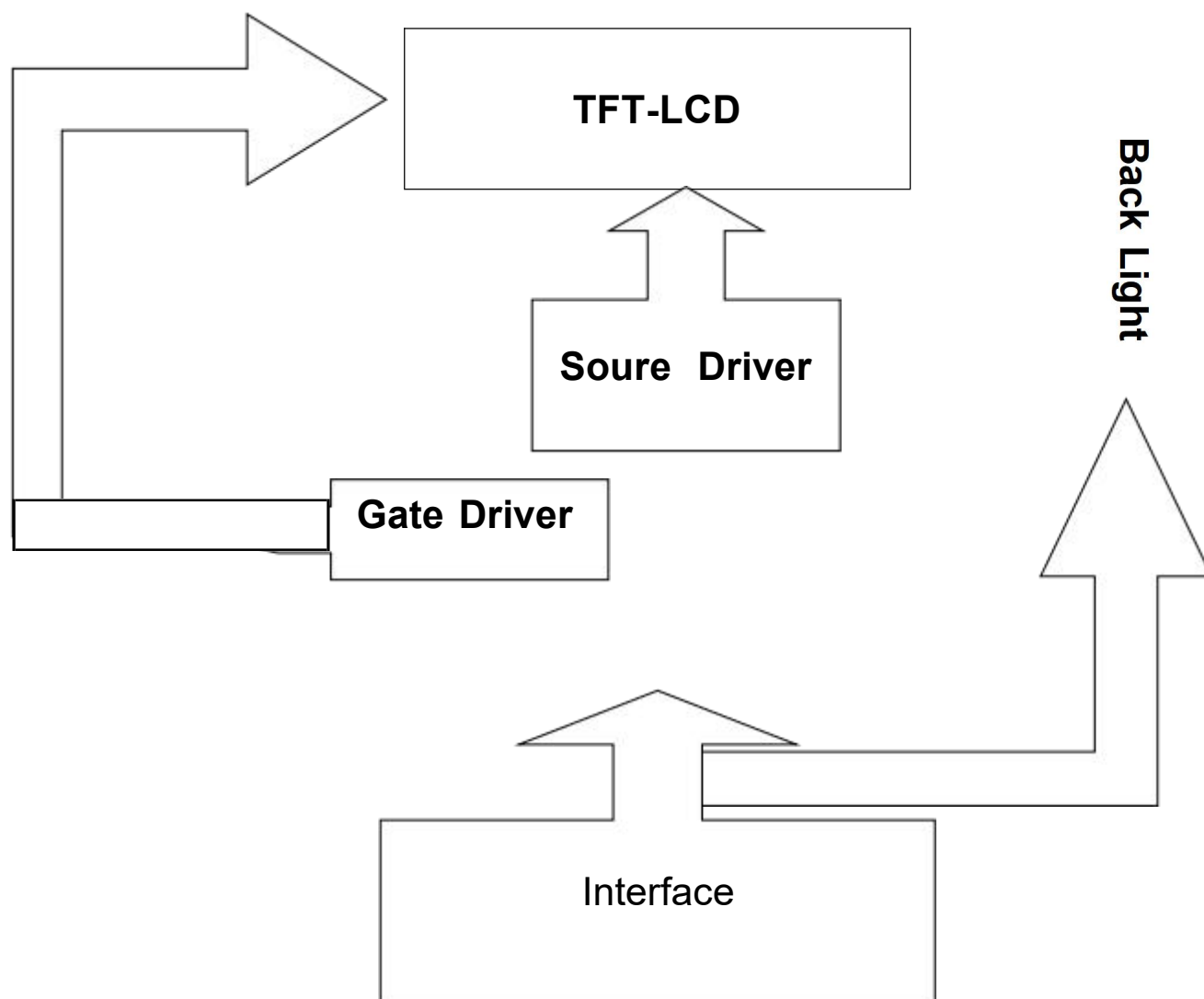
Item	Symbol	Min	Max	Unit	Remark
Supply voltage	VCC	-0.3	4.8	V	Note1 Note2
Supply voltage	IOVCC	-0.3	4.6	V	
Operating temperature	TOPR	-20	70	°C	
Storage temperature	TSTR	-30	80	°C	

6. Electrical Characteristics

Item		Symbol	Rating			Unit	Remark
			Min	Typ	Max		
Supply voltage		VCC	---	2.8	---	V	Note1
Supply voltage		IOVCC	---	1.8	---	V	
Input Voltage	L level	VIL	0	---	0.3*IOVCC	V	
	H level	VIH	0.7*IOVCC	---	IOVCC	V	

7. Module Function Description

7-1. Block Diagram Of LCM



7-2. Pin Description

PinNo.	Symbol	Function
1	GND	Ground
2	VCC	Power Supply. 2.8V
3	SDA	SPI interface input/output pin
4	SCLK	This pin is used to be serial interface clock
5	CS	Chip select input pin
6	RESET	External reset input
7	LEDA	LED back light(Anode)
8	LEDK	LED back light(Cathode)
9	NC	NC
10	RS	Display data/command selection pin in 4-line serial interface

7-3 Timing Characteristics

7.4.3 Serial Interface Characteristics (4-line serial):

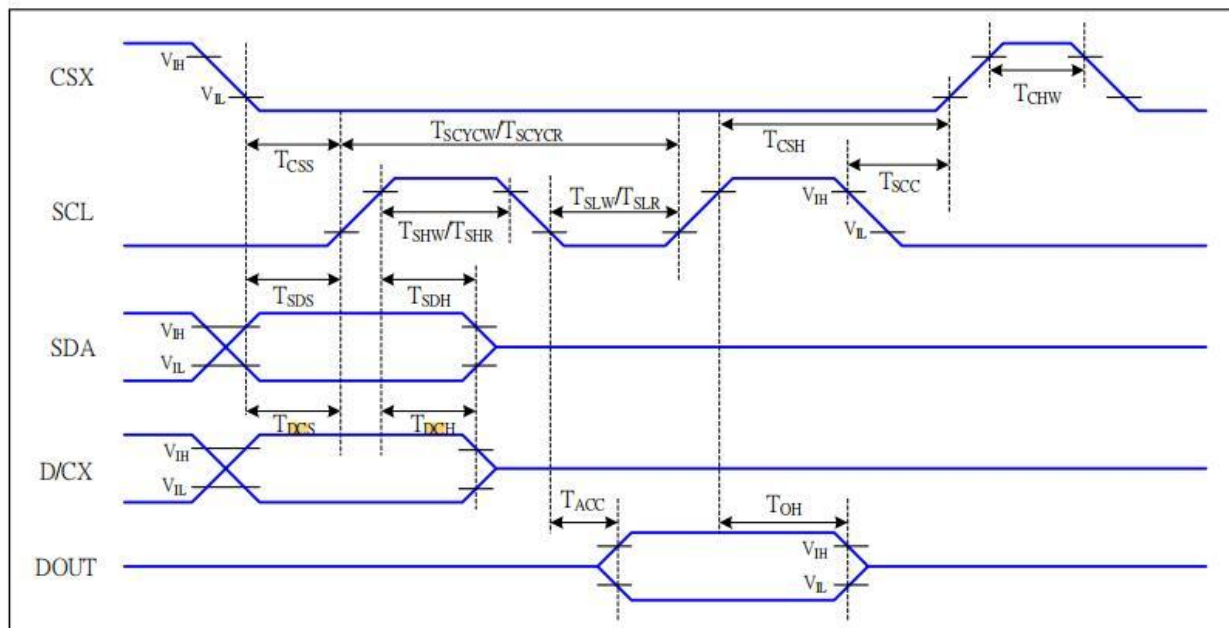


Figure 5 4-line serial Interface Timing Characteristics

$V_{DDI}=1.65$ to $3.3V$, $V_{DD}=2.4$ to $3.3V$, $AGND=DGND=0V$, $T_a=25^{\circ}C$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	16		ns	-write command & data ram
	T_{SHW}	SCL "H" pulse width (Write)	7		ns	
	T_{SLW}	SCL "L" pulse width (Write)	7		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T_{DCS}	D/CX setup time	10		ns	
	T_{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T_{SDS}	Data setup time	7		ns	
	T_{SDH}	Data hold time	7		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum $CL=30pF$
	T_{OH}	Output disable time	15	50	ns	For minimum $CL=8pF$

Table 6 4-line serial Interface Characteristics

8. Electro-Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response time		Tr +Tf	$\theta_x = \theta_y = 0$	---	30	35	ms	Note 1
Contrast Ratio		CR		---	1200	---	---	Note 2
Color Chromaticity (CIE1931)	White	W x	$\theta_x = \theta_y = 0$	---	0.308	---	---	
		W y		---	0.339	---	---	
Viewing angle	θ_T		CR > 10	---	80	---	Deg.	Note 3
	θ_B			---	80	---		
	θ_L			---	80	---		
	θ_R			---	80	---		
Luminance ($I_F = 80mA$)		L			300	---	cd/m2	Note4

3.2 Measuring Condition

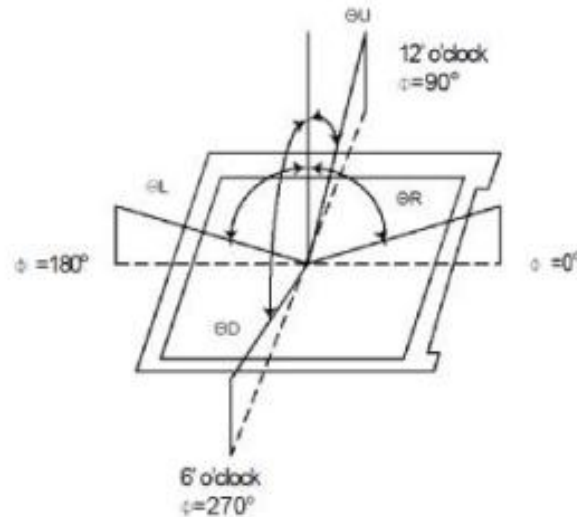
- Measuring surrounding: dark room
- LED current I_L : 80mA
- Ambient temperature: $25 \pm 2^\circ\text{C}$
- 15min. warm-up time

3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7 for other optical characteristics.
- Measuring spot size: 20 ~ 21 mm

Note (3) Definition of Viewing Angle:

Note (1) Definition of Viewing Angle

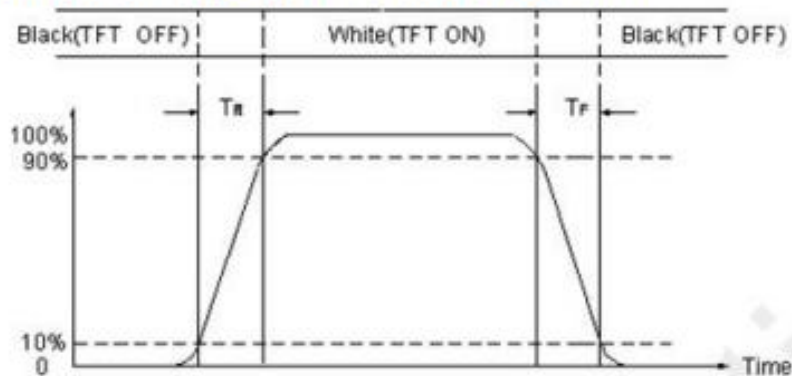


Note (2) Definition of Contrast Ratio(CR):

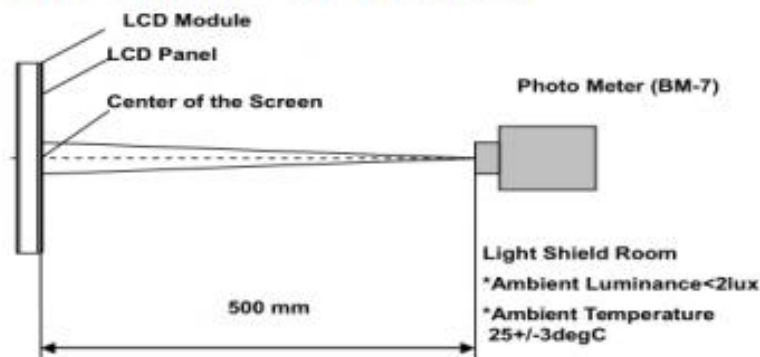
Measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3) Definition of Response Time: Sum of TR and TF

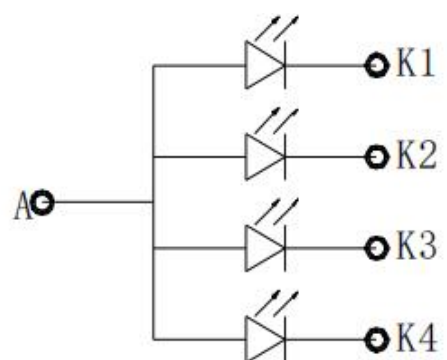


Note (4) Definition of optical measurement setup



Note (5) Definition of brightness uniformity

Note(4) Backlight circuit



80mA, 3.2V

9.Records Of Version

Version	Revise Date	Page	Content
00	2024-4-22	ALL	New released